

A METHOD AND SYSTEM FOR IMPLEMENTING A FLOATING POINT COMPARE
USING RECORDED FLAGS

ABSTRACT OF THE DISCLOSURE

5 A method and system for implementing a floating point compare operation in
an x86 compatible processor. The method includes the step of comparing a first bit
pattern and a second bit pattern using a floating point unit of an x86 compatible
processor. The EFLAGS register is set in accordance with a result of the
comparison. A sign flag and an overflow flag of the EFLAGS register are encoded
10 with information derived from the result of the comparison.